

**RELIABILITY AND FAILURE ANALYSIS OF BIPOLAR JUNCTION  
TRANSISTOR (BJT)**

**LIM CHING YIT**

**THIS DISSERTATION IS SUBMITTED IN PARTIAL FULFILMENT OF  
THE BACHELOR DEGREE IN SCIENCE WITH HONOURS**

**PERPUSTAKAAN  
UNIVERSITI MALAYSIA SABAH**

**PHYSICS WITH ELECTRONICS PROGRAMME  
SCHOOL OF SCIENCE AND TECNOLOGY  
UNIVERSITI MALAYSIA SABAH**

**APRIL 2008**

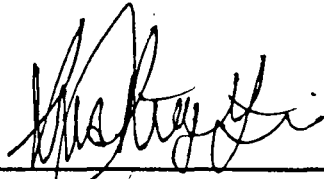


**UMS**  
UNIVERSITI MALAYSIA SABAH

## DECLARATION

I hereby declare that this project is my own work, except for certain quotations and reference that have been duly cited.

**4 April 2008**



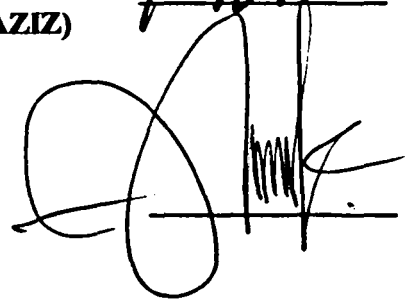
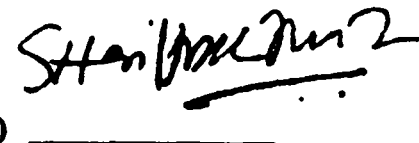
---

LIM CHING YIT  
HS 2005-2188



**AUTHORISED BY**

Signature

**1. SUPERVISOR****(MR. SAAFIE SALLEH)****2. EXAMINER 1****(PROF. MADYA DR. FAUZIAH HJ. ABDUL AZIZ)****3. EXAMINER 2****(MR. ALVIE LO SIN VOI)****4. DEAN****(PROF. MADYA DR. SHARIFF A. KADIR S. OMANG)**

## ACKNOWLEDGEMENT

First of all, I would like to thanks my supervisor, Mr. Saafie Salleh who help and guide me a lot in order for me to complete my final year project. He spent a lot of his precious time with me to discuss every single details of my project especially my project's results and methodology.

Besides, I would like to thanks physics with electronics laboratory assistant, Mr. Rahim who advises me in my laboratory works and prepare the equipment needed in this experiment. I also appreciate all my friends in Physics with Electronics Programme who work hard together with me.

Furthermore, I would like to thanks my family members who gave me financial and mental supports in order to complete my final year project. Finally, the helpfulness and kindness of everyone who directly or indirectly involved in my project will be remembered forever.

Thanks.



## ABSTRACT

This research is a failure analysis for bipolar junction transistor (BJT) to observe the changes of current gain,  $\beta$  with the increase of input voltage on transistor in different frequency in an amplifier circuit. We also observe the changes of current gain with increase of temperature effect on transistor. 2N2222A n-channel enhancement BJT transistor was used. This transistor in the amplifier circuit is to test on it when the function of transistor meets the failure. Multimeter was connected in the circuit to get the base current and collector current. These parameters are important as it use to calculate the current gain of the circuit. The values of the current are recorded from each increment of 0.1V of input voltage starting from 1.0V until the transistor meets the failure point. The process is repeated for different frequency from 2 kHz to 20 kHz. The values of the current are also recorded for every 5 °C temperature increase starting from 25 °C until 100 °C . This process is repeated for different transistor. Graph of current gain versus input voltage and current gain versus temperature are plotted. From the graph, it showed that transistor operated in higher frequency will meet the failure point at lower voltage input with increasing input voltage. It also showed that the transistor function normally within the range of 100 °C . Through the research, we found that the internal temperature is increasing while operating under high voltage and frequency as high electrical and thermal stresses at the semiconductor surface of transistor and cause the transistor to the failure. From the experiment, we conclude that the current gain in the amplifier circuit work well and efficient at the range of input between 1.3V and 2.6V. Failure point ( $\beta \approx 0$ ) decreased in the input voltage range from 4.2V to 3.6V when frequency increased.



## ABSTRAK

Kajian ini merupakan satu analisis kegagalan transistor (BJT) di mana pemerhatian nilai gandaan arus,  $\beta$  terhadap kesan kenaikan voltan masukan,  $V_{in}$  dengan frekuensi tinggi yang berlainan pada litar amplifier. Pemerhatian juga dilakukan terhadap nilai gandaan arus dengan kenaikan suhu pada transistor. Transistor yang digunakan ialah transistor 2N2222A iaitu transistor jenis BJT perluasan saluran-n. Transistor dalam litar amplifier diuji dari keadaan berfungsi normal sampai ia menemui kegagalan. Multimeter digital disambungkan dalam litar untuk mendapatkan arus tapak,  $I_b$  dan arus pemungut,  $I_c$ . Parameter-parameter ini penting untuk mengira gandaan arus dalam litar. Nilai-nilai arus tersebut diambil bagi setiap peningkatan 0.1V bagi voltan masukan yang bermula dari 1.0V sehingga transistor menemui titik kegagalan. Keadaan diulang bagi beberapa frekuensi tinggi bermula dari 2 kHz sehingga 20 kHz. Nilai-nilai arus juga dicatat bagi setiap peningkatan 5°C kenaikan suhu bermula dari 25°C sehingga 100°C. Keadaan ini diulang bagi transistor yang berlainan. Graf gandaan arus melawan voltan masukan dan gandaan arus melawan suhu diplotkan. Melalui graf, didapati bahawa transistor yang beroperasi dalam frekuensi tinggi akan menemui titik kegagalan pada voltan masukan yang lebih awal dengan kenaikan voltan masukan. Selain itu, didapati bahawa transistor dapat berfungsi secara biasa dalam lingkungan 100°C. Melalui kajian, didapati bahawa haba dalaman transistor dinaikkan apabila ia beroperasi dalam voltan dan frekuensi tinggi kerana elektrik tinggi dan tekanan haba pada permukaan semikonduktor transistor yang mengakibatkan transistor gagal berfungsi. Melalui eksperimen, dapat disimpulkan bahawa litar amplifier dapat berjalan dengan baik dan mempunyai gandaan arus yang paling efisien dalam lingkungan 1.3V dan 2.6V. Titik kegagalan ( $\beta \approx 0$ ) diawalkan dalam voltan masukan dari 4.2V sehingga 3.6V apabila frekuensi ditingkatkan.



## TABLE OF CONTENT

	Pages
DECLARATION	ii
AUTHORISED BY	iii
ACKNOWLEDGEMENT	iv
ABSTRACT	v
ABSTRAK	vi
TABLE OF CONTENT	vii
LIST OF TABLES	ix
LIST OF FIGURES	x
LIST OF PHOTOS	xii
LIST OF SYMBOLS	xiii
LIST OF APPENDIX	xiv
 <b>CHAPTER 1          INTRODUCTION</b>	
1.1    INTRODUCTION	1
1.2    PROJECT PURPOSE	2
1.3    PROJECT OBJECTIVES	2
1.4    PROJECT SCOPE	3
1.5    HYPOTHESIS	3
 <b>CHAPTER 2          LITERATURE REVIEW</b>	
2.1    INTRODUCTION	4
2.2    BIPOLAR JUNCTION TRANSISTOR	5
2.3    OPERATION OF THE NPN BIPOLAR JUNCTION TRANSISTOR	6
2.4    TEMPERATURE EFFECTS IN BIPOLAR JUNCTION TRANSISTOR	10
2.5    BJT AS AN AMPLIFIER	11
2.6    BJT AS A SWITCH	13
2.7    TRANSISTOR MAXIMUM RATINGS	15
2.8    NON-IDEAL EFFECTS	16
2.8.1    Base-Width Modulation Effects	16
2.8.2    Recombination in the Depletion Layers	18



2.8.3	High Injection Effects	19
2.8.4	Base Spreading Resistance and Emitter Current Crowding	20

### **CHAPTER 3            METHODOLOGY**

3.1	INTRODUCTION	22
3.2	MATERIALS AND EQUIPMENT	22
3.3	TRANSISTOR TESTING	23
3.4	CIRCUIT CONFIRMATION	24
3.5	EXPERIMENT DATA	25
3.5.1	Method	26
3.5.2	Tabulation Data	26
3.5.3	Graph Plotting	27

### **CHAPTER 4            RESULTS AND DATA ANALYSIS**

4.1	EXPERIMENT RESULT	28
4.2	DATA ANALYSIS	28
4.3	GRAPH ANALYSIS	29
4.4	DISCUSSION	39
4.5	EXPERIMENT UNCERTAINTY	42

### **CHAPTER 5            CONCLUSION**

REFERENCES	46
APPENDIX A	48
APPENDIX B	54
APPENDIX C	67





## LIST OF TABLES

Number of Table	Pages
4.1    Analysis on transistor with increasing voltage in different frequencies	39
B1    Data collected of transistor in 2 kHz frequency	54
B2    Data collected of transistor in 4 kHz frequency	55
B3    Data collected of transistor in 6 kHz frequency	56
B4    Data collected of transistor in 8 kHz frequency	57
B5    Data collected of transistor in 10 kHz frequency	58
B6    Data collected of transistor in 12 kHz frequency	59
B7    Data collected of transistor in 14 kHz frequency	60
B8    Data collected of transistor in 16 kHz frequency	61
B9    Data collected of transistor in 18 kHz frequency	62
B10   Data collected of transistor in 20 kHz frequency	63
B11   Data collected for first transistor in temperature test	64
B12   Data collected for second transistor in temperature test	64
B13   Data collected for third transistor in temperature test	65
B14   Data collected for fourth transistor in temperature test	65
B15   Data collected for fifth transistor in temperature test	66



## LIST OF FIGURES

Number of Figure	Pages
2.1 Structure and sign convention of a NPN bipolar junction transistor	5
2.2 Schematic diagrams for both NPN and PNP transistors	5
2.3 Electron and hole flow under forward active bias, $V_{BE} > 0$ and $V_{BC} = 0$	6
2.4 Energy band diagram of a bipolar transistor biased in the forward active mode	7
2.5 Basic transistor amplifier AC circuit	11
2.6 Ideal switching action of a transistor in cutoff condition	13
2.7 Ideal switching action of a transistor in saturation condition	13
2.8 Variation of the minority-carrier distribution in the base quasi-neutral region due to a variation of the base-collector voltage	16
2.9 Collector current increases with an increase of the collector-emitter voltage due to the Early effect	17
2.10 Gummel plot: Collector current (top curve) and base current (bottom curve) of a silicon bipolar transistor versus the base-emitter voltage	18
3.1 Circuit used in the experiment	24
4.1 Graph current gain versus input voltage with 2 kHz frequency	30
4.2 Graph current gain versus input voltage with 4 kHz frequency	30
4.3 Graph current gain versus input voltage with 6 kHz frequency	31
4.4 Graph current gain versus input voltage with 8 kHz frequency	32
4.5 Graph current gain versus input voltage with 10 kHz frequency	32
4.6 Graph current gain versus input voltage with 12 kHz frequency	33
4.7 Graph current gain versus input voltage with 14 kHz frequency	34
4.8 Graph current gain versus input voltage with 16 kHz frequency	34
4.9 Graph current gain versus input voltage with 18 kHz frequency	35
4.10 Graph current gain versus input voltage with 20 kHz frequency	35
4.11 Graph current gain versus temperature with first transistor	36
4.12 Graph current gain versus temperature with second transistor	37



4.13	Graph current gain versus temperature with third transistor	37
4.14	Graph current gain versus temperature with fourth transistor	38
4.15	Graph current gain versus temperature with fifth transistor	38
4.16	Graph failure point of transistor versus frequency	40
4.17	Graph maximum of the current gain with different frequencies	40



## LIST OF PHOTOS

Number of photo	Pages
3.1 Real diagram of the circuit used in the experiment	25
C1 Digital multimeters	67
C2 Function generator and DC supply	67
C3 Hot-plate	68
C4 Experiment set for high voltage and high frequency	68
C5 Experiment set for high temperature	69



## LIST OF SYMBOLS

$\alpha$	Transport factor
$\beta$	Current gain
$\gamma_E$	Emitter efficiency
$\delta_r$	Depletion layer recombination factor
$A_v$	Voltage gain
$I$	Current
$I_B$	Base current
$I_C$	Collector current
$I_E$	Emitter current
$I_{E,n}$	Electron diffusion current
$I_{E,p}$	Hole diffusion current
$I_{r,d}$	Base-emitter depletion layer recombination current
$I_{r,B}$	Base recombination current
$I_{CBO}$	Leakage current
$R$	Resistor
$R_C$	Collector resistor
$R_E$	Emitter resistor
$V$	Voltage
$V_{BE}$	Base-emitter voltage
$V_{CB}$	Collector-base voltage
$V_{CE}$	Collector-emitter voltage
$V_{BB}$	Forward biased voltage
$V_{CC}$	Reverse biased voltage
$V_{in}$	Input voltage
$^{\circ}C$	Celsius

## LIST OF APPENDIX

APPENDIX A	Data sheet of transistor 2N2222A
APPENDIX B	Data Collected from the experiment
APPENDIX C	Material and equipment photos



## CHAPTER 1

### INTRODUCTION

#### 1.1 INTRODUCTION

In 1947, W. H. Brattain and J. Bardeen, both of Bell Labs, brought two closely spaced metallic needles into contact with the same germanium “base” wafer. The two needles served as electrodes, which were termed the *emitter* and *collector* terminals. A third, *base* terminal was also attached to the germanium crystal. Their experiments demonstrated that varying either the emitter or base dc terminal currents could cause a proportional variation in the collector voltage. Since an input (base or emitter) current could be used to control the output (collector) voltage, the gain (input-to-output transfer) of the device is equal to output voltage divided by input current.

The units of the gain defined by equation above would be ohms. Hence J. R. Pierce (also of Bell Labs) described the device as a transfer resistor and coined the term transistor. These first transistors were described as “point contact” devices. The point contact transistors were extremely difficult to produce commercially. However, in 1949 William Shockley theoretically described the bipolar junction transistor, or BJT.



The BJT truly revolutionized the electronics industry. Not only did it serve to render the vacuum tube obsolete, but the search for improved transistor manufacturing techniques laid the groundwork for the modern integrated-circuit fabrication.

In a similar fashion, the linear integrated circuit is also pushing the discrete BJT into obsolescence in many applications. However, at present, we find that many new electronics circuit designs are a combination of both linear integrated circuits and discrete transistors. Consequently, we need to be reasonably comfortable with both of these electronic “tools”. Further, an understanding of the discrete transistor can provide us with an intuitive understanding of the operation and the limitations of linear (and digital) integrated circuits. (Stephen R. Fleeman, 1990)

## **1.2 PROJECT PURPOSE**

The main purpose of this project is to build a simple circuit with bipolar junction transistor (BJT) and test the reliability and failure analysis of the BJT component.

## **1.3 PROJECT OBJECTIVES**

- i To build a simple BJT test circuit.
- ii To analysis the current gain of a BJT at various high voltages.
- iii To analysis the current gain of a BJT at various high frequencies.
- iv To analysis the current gain of a BJT at various temperatures.





## **1.4 PROJECT SCOPE**

The reliability and failure analysis will only focus on three common stresses used to accelerate device failure which is voltage frequency and temperature.

## **1.5 HYPOTHESIS**

Each research done is based on theories learned and the theories learned are actually as a guide in doing scientific experiments and researches. Hypothesis is a tentative explanation for an observation, phenomenon, or scientific problem that can be tested by further investigation. In this research, the hypothesis is the higher temperature, the more active the BJT. However, after a certain temperature, the BJT would fail. Besides, The higher frequency, the BJT performance would be affected. Then, the higher voltage (or current), the easier the BJT would fail.

## **CHAPTER 2**

### **LITERATURE REVIEW**

#### **2.1 INTRODUCTION**

The transistor was invented by a team of three men at Bell Laboratories at 1947. Although this first transistor was not a bipolar junction device, it was the beginning of a technological revolution that is still continuing. All the complex electronic devices and system today are an outgrowth of early developments in semiconductor transistors.

There are two basic types of transistors in the market which are the bipolar junction transistor (BJT), and the field-effect transistor (FET). However, both transistors have many differences in their structures and characteristics. BJT is so called bipolar junction transistor is because it contains both flow of majority carriers and minority carriers while there is only flow of majority carriers in FET. Another distinct difference between the analysis of BJT and FET is that the input controlling variable for a BJT transistor is a current level, while for the FET a voltage is the controlling variable.



## 2.2 BIPOLAR JUNCTION TRANSISTOR

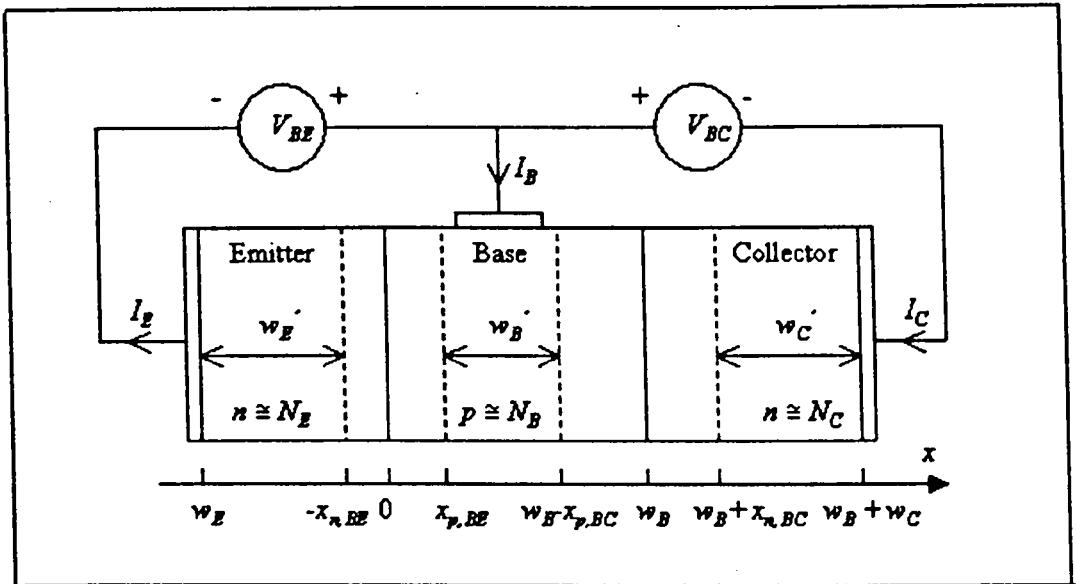


Figure 2.1 Structure and sign convention of a NPN bipolar junction transistor.

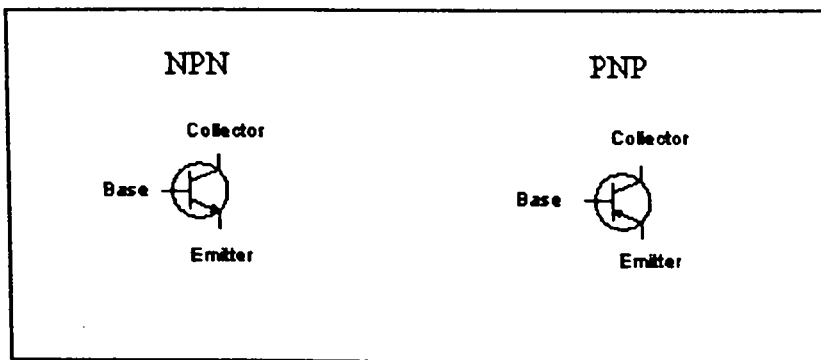
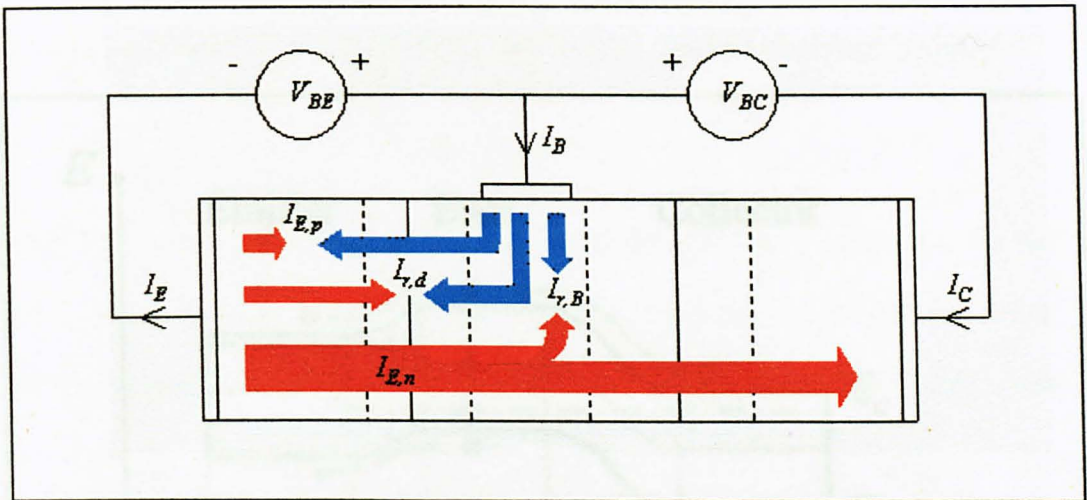


Figure 2.2 Schematic diagrams for both NPN and PNP transistors.

A bipolar junction transistor consists of two back-to-back p-n junctions, who share a thin common region with width,  $w_B$ . Contacts are made to all three regions, the two outer regions called the emitter and collector and the middle region called the base.

The device is called bipolar since its operation involves both types of mobile carriers, electrons (negative charge) and holes (positive charge). In Figure 2.2, the arrow on the emitter of NPN transistor shows that the current is easier to flow from emitter. Meanwhile, the arrow on the emitter of PNP transistor shows the direction which easier flow by positive charge (direction of current). (Cook, 1997)

### 2.3 OPERATION OF THE NPN BIPOLAR JUNCTION TRANSISTOR



**Figure 2.3** Electron and hole flow under forward active bias,  $V_{BE} > 0$  and  $V_{BC} = 0$ .

Since the device consists of two back-to-back diodes, there are depletion regions between the quasi-neutral regions. The width of the quasi neutral regions in the emitter, base and collector are indicated with the symbols  $w_E'$ ,  $w_B'$  and  $w_C'$  and are calculated from:

$$w_E' = w_E - x_{n, BE} \quad (2.1)$$

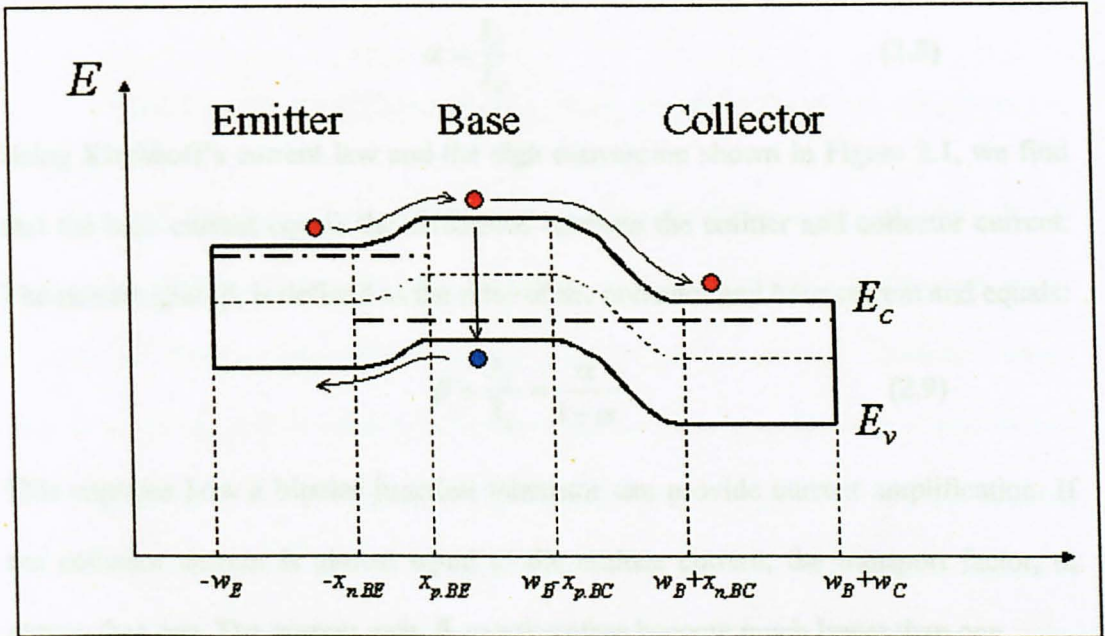
$$w_B' = w_B - x_{p, BE} - x_{p, BC} \quad (2.2)$$

$$w_C' = w_C - x_{n,BC} \quad (2.3)$$

The emitter current  $I_E$ , the collector current  $I_C$ , and the base current  $I_B$  (electron flows) have been indicated in Figure 2.3. By Kirchhoff's current law:

$$I_E = I_C + I_B \quad (2.4)$$

The base-emitter voltage and the base-collector voltage are positive if a positive voltage is applied to the base contact relative to the emitter and collector respectively. (Herman, 2000)



**Figure 2.4** Energy band diagram of a bipolar transistor biased in the forward active mode.

The total emitter current is the sum of the electron diffusion current,  $I_{E,n}$ , the hole diffusion current,  $I_{E,p}$  and the base-emitter depletion layer recombination current,  $I_{r,d}$  (Gray and Meyer, 1993).

$$I_E = I_{E,n} + I_{E,p} + I_{r,d} \quad (2.5)$$

The total collector current is the electron diffusion current,  $I_{E,n}$ , minus the base recombination current,  $I_{r,B}$  (Gray and Meyer, 1993).

$$I_C = I_{E,n} - I_{r,B} \quad (2.6)$$

The base current is the sum of the hole diffusion current,  $I_{E,p}$ , the base recombination current,  $I_{r,B}$  and the base-emitter depletion layer recombination current,  $I_{r,d}$  (Gray and Meyer, 1993).

$$I_B = I_{E,p} + I_{r,B} + I_{r,d} \quad (2.7)$$

The transport factor,  $\alpha$ , is defined as the ratio of the collector and emitter current:

$$\alpha = \frac{I_C}{I_E} \quad (2.8)$$

Using Kirchhoff's current law and the sign convention shown in Figure 2.1, we find that the base current equals the difference between the emitter and collector current. The current gain,  $\beta$ , is defined as the ratio of the collector and base current and equals:

$$\beta = \frac{I_C}{I_B} = \frac{\alpha}{1 - \alpha} \quad (2.9)$$

This explains how a bipolar junction transistor can provide current amplification. If the collector current is almost equal to the emitter current, the transport factor,  $\alpha$ , approaches one. The current gain,  $\beta$ , can therefore become much larger than one.

To facilitate further analysis, we now rewrite the transport factor,  $\alpha$ , as the product of the emitter efficiency,  $\gamma_E$ , the base transport factor,  $\alpha_T$ , and the depletion layer recombination factor,  $\delta_r$ .

$$\alpha = \alpha_T \gamma_E \delta_r \quad (2.10)$$



The emitter efficiency,  $\gamma_E$ , is defined as:

$$\gamma_E = \frac{I_{E,n}}{I_{E,n} + I_{E,p}} \quad (2.11)$$

The base transport factor,  $\alpha_T$ , is defined as:

$$\alpha_T = \frac{I_{E,n} - I_{r,B}}{I_{E,n}} \quad (2.12)$$

The depletion layer recombination factor,  $\delta_r$ , is defined as:

$$\delta_r = \frac{I_E - I_{r,d}}{I_E} \quad (2.13)$$

## 2.4 TEMPERATURE EFFECTS IN BIPOLAR JUNCTION TRANSISTOR

The temperature dependence of bipolar transistors depends on a multitude of parameters affecting the bipolar junction transistor characteristics in different ways. First we will discuss the temperature dependence of the current gain. Since the current gain depends on both the emitter efficiency and base transport factor, we will discuss these separately. The emitter efficiency depends on the ratio of the carrier density, diffusion constant and width of the emitter and base. As a result, it is not expected to be very temperature dependent. The carrier densities are linked to the doping densities. Barring incomplete ionization, which can be very temperature dependent, the carrier densities are independent of temperature as long as the intrinsic carrier density does not exceed the doping density in either region. The width is very unlikely to be temperature dependent and therefore also the ratio of the emitter and base width.

The ratio of the mobility is expected to be somewhat temperature dependent due to the different temperature dependence of the mobility in n-type and p-type material. The base transport is more likely to be temperature dependent since it depends on the product of the diffusion constant and carrier lifetime. The diffusion constant in turn equals the product of the thermal voltage and the minority carrier mobility in the base. The recombination lifetime depends on the thermal velocity. The result is therefore moderately dependent on temperature.

Typically the base transport reduces with temperature, primarily because the mobility and recombination lifetime are reduced with increasing temperature. Occasionally the transport factor initially increases with temperature, but then reduces again.

$$I_{CBO}(T) = [I_{CBO}(25^{\circ}\text{C})][2^{(T-25)/8}] \quad (2.14)$$

The importance of the effects of temperature on the operation of the BJT cannot be overstated. Temperature variations can cause severe shifts in the Q-point of a BJT amplifier. This could possibly result in signal distortion. As we shall see, temperature effects cause the ac parameters of a BJT to change. Consequently, quantities such as a voltage amplifier's voltage gain and input impedance will also be affected by temperature. (Fleeman, 1990)



## REFERENCES

- Blocki, J. 2006. The Thermal Failure Analysis of Microstrip Silicon Detectors. *Journal of Nuclear Instruments and Methods in Physics Research A* **564**, 197-203.
- Boylestad, R. L. & Nashelsky, L. 1982. *Electronic Dvices And Circuit Theory*. 3<sup>rd</sup> ed. Prentice Hall, United States of America.
- Chang, P. C., Jang, S. L. & Chen, Y. S. 1994. Degradation of Bipolar Junction Transistors under Dynamic High Current Stress and Biased in Open-collector Condition. *Journal of Solid-State Electronics* **37**, 303-309.
- Cook, N. P. 1997. *Practical Electronics*. Prentice Hall, United States of America.
- Fleeman, S.R. 1990. *Electronic Devices: Discrete and Integrated*. Prentice Hall, United States of America.
- Floyd, T. L. 1996. *Electronic Devices*. 4<sup>th</sup> ed. Prentice Hall, United States of America.
- Floyd, T. L. 2004. *Electronics Fundamentals: Circuits, Devices, and Applications*. 6<sup>th</sup> ed. Prentice Hall, United States of America.
- Gray, P. R. & Meyer, R. G. 1993. *Analysis and Design of Analog Integrated Circuits*. 3<sup>rd</sup> ed. John Wiley & Son, London.



Hambley, A. R. 2000. *Electronics*. 2<sup>nd</sup> ed. Prentice Hall, United States of America.

Herman, S. L. 2000. *Electronics for Electricians*. 4<sup>th</sup> ed. Delmar Thomson Learning, Canada.

Jensen, F. 1995. *Electronic Component Reliability*. John Wiley & Sons, England.

Kececioglu, D. & Feng, B. S. 1997. *Burn-In Testing: Its Quantification and Optimization*. Prentice Hall, United States of America.

Klion, J. 1992. *Practical Electronic Reliability Engineering: Getting the Job Done from Requirement through Acceptance*. Van Nostrand Reinhold, New York.

Pecht, M. 1995. *Product Reliability, Maintainability, and Supportability Handbook*. CRC Press, United States of America.

Sheng, S. R., McAlister, S. P., Storey, C., Lee, L. S. & Hwang, H. P. 2002. Hot-carrier Induced Degradation and Recovery in Polysilicon-emitter Bipolar Transistors. *Journal of Solid-State Electronics* 46, 1603-1608.

Velardi, F., Iannuzzo, F., Busatto, G., Porzio, A., Sanseverino, A., Curro, G., Cascio, A. & Frisina, F. 2004. The Role of The Parasitic BJT Parameters on The Reliability of New Generation Power MOSFET during Heavy Ion Exposure. *Journal of Microelectronics Reliability* 44, 1407-1411.

